

Electronic Acknowledgement Receipt

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| EFS ID: | 2145534 |
| Application Number: | 10817201 |
| International Application Number: | |
| Confirmation Number: | 2174 |
| Title of Invention: | Variable threshold transistor for the Schottky FPGA and multilevel storage cell flash arrays |
| First Named Inventor/Applicant Name: | Augustine W. Chang |
| Customer Number: | 29141 |
| Filer: | Patrick T. Bever/Rebecca Baumann |
| Filer Authorized By: | Patrick T. Bever |
| Attorney Docket Number: | 3065P |
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| Application Type: | Utility under 35 USC 111(a) |

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| Submitted with Payment | no |
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File Listing:

| Document Number | Document Description | File Name | File Size(Bytes) /Message Digest | Multi Part /.zip | Pages (if appl.) |
|-----------------|----------------------|----------------------------|---|------------------|------------------|
| 1 | Power of Attorney | SUP-049-ExecutedPOARVK.pdf | 126887 4b0490d7eb9a97d1 8521efa3fb7c6e9c afcc21d0 | no | 2 |

Warnings:

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| Total Files Size (in bytes): | 126887 |
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